

# EI E 482: Digital CMOS VLSI Design Electrical and Computer Engineering

Design, layout, simulation, and test of design custom digital CMOS/VLSI chips, using a CMOS cell library and state-of-the-art CAD tools. Digital CMOS static and dynamic gates, flip flops, CMOS array structures commonly used in digital systems.

3 Credits

## Prerequisites

- EI E 385: Advanced Digital Systems
- Pre-Requisite: 24 Earned Hours

#### **One-way corequisites**

• El E 351: Electronics Circuits I

# Instruction Type(s)

• Lecture: Lecture for EI E 482

## **Subject Areas**

- <u>Computer Hardware Engineering</u>
- Electrical and Electronics Engineering

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