El E 482: Digital CMOS VLSI Design

Electrical Engineering

Design, layout, simulation, and test of design custom digital CMOS/VLSI chips, using a CMOS cell library and state-of-the-art CAD tools. Digital CMOS static and dynamic gates, flip flops, CMOS array structures commonly used in digital systems.

3 Credits

Prerequisites

- El E 385: Advanced Digital Systems

Instruction Type(s)

- Lecture: Lecture for El E 482

Course Fee(s)

Electrical Eng

- $50.00 when booked for:
  - Spring, Second Spring, May, Full Summer, First Summer, Second Summer, August 2016-17
  - Fall, First Fall, Second Fall, Winter, Spring, First Spring, Second Spring 2017-18

Subject Areas

- Computer Hardware Engineering
- Electrical and Electronics Engineering

Related Areas

- Computer Engineering, General
- Electrical, Electronics and Communications Engineering, Other
- Laser and Optical Engineering
- Telecommunications Engineering