EI E 482: Digital CMOS VLSI Design

Electrical Engineering

Design, layout, simulation, and test of design custom digital CMOS/VLSI chips, using a CMOS cell library and state-of-the-art CAD tools. Digital CMOS static and dynamic gates, flip flops, CMOS array structures commonly used in digital systems.

3 Credits

Prerequisites
- EI E 351: Electronics Circuits I

Instruction Type(s)
- Lecture: Lecture for EI E 482

Subject Areas
- Computer Hardware Engineering
- Electrical and Electronics Engineering

Related Areas
- Computer Engineering, General
- Electrical, Electronics and Communications Engineering, Other
- Laser and Optical Engineering
- Telecommunications Engineering